

Reg. No:

--	--	--	--	--	--	--	--	--	--

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)**B.Tech III Year II Semester Regular & Supplementary Examinations October-2020**
OBJECT ORIENTED ANALYSIS AND DESIGN

(Common to CSE & CSIT)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

- 1 a List and explain the principles of modeling. **6M**
b What are rules of unified modeling language? **6M**

OR

- 2 a Discuss the building blocks of the UML. **6M**
b Describe object oriented modeling. **6M**

UNIT-II

- 3 a Explain stereotypes, tagged values, and constraints with examples. **8M**
b Describe the importance of packages in the UML. **4M**

OR

- 4 a Explain modeling single Inheritance with an example. **7M**
b Discuss briefly about advanced relationships. **5M**

UNIT-III

- 5 a Explain roles, links, messages, actions and sequences of interactions. **8M**
b Build the modeling flows of control. **4M**

OR

- 6 a Describe the terms and concepts of use case diagrams. **6M**
b Design a use case diagram to show ATM system. **6M**

UNIT-IV

- 7 a Explain the importance of modeling the lifetime of an object. **5M**
b List the common modeling techniques for processes and threads. **7M**

OR

- 8 a Describe the concepts of a state machine. **7M**
b Write a note on the advanced states and transitions. **5M**

UNIT-V

- 9 a Distinguish between modeling tables, files, and documents. **7M**
b Explain the modeling executables and libraries. **5M**

OR

- 10 a Justify the term Standard Elements in components. **6M**
b Design a component diagram for Hostel management. **6M**

*** END ***